

P/N	NFB-200G2QL
On-board components	
FPGA chip	Xilinx Virtex UltraScale+ (XCVU7P mounted, pin compatibility with VU5P and VU9P)
Firmware management	CPLD + FLASH 2x 512Mb (x16), 133MHz
Memory options	No QDR / 3x QDRIIIe 72Mb / 3x QDRIIIe 288Mb (possibility to mount up to 576Mb modules) 675MHz, latency cca 4.5ns, max. bandwidth 145.8Gb/s
Oscillator	PLL programmable clock generator (+/- 100ppb)
Interfaces	
Network	2x QSFP28
Host	PCI Express Gen 3, Gen 4 x16 + 1x expander x16
Other	Pulse per second input, LED indicators for QSFP28 cages
Power supply	Via PCI Express connector
Physical properties	
Form factor	PCI Express: low profile, half-length
Dimensions	Low profile 68.9mm (2.71 inches) max. Half length 167.65 mm (6.6 inches) max. Compliant with PCI Express® Card Electromechanical Specification Rev. 4.0

